

REMARKS

The Examiner has provisionally rejected claims 1, 2, 4-8, 10-14 and 16-22 under 35 U.S.C. 101 as claiming the same invention as that of claims 21-42 of copending Application No. 10/740,036.

This rejection was rendered moot. For example, independent claim 1 includes a recitation of apparatus that cooperates "with the OS scheduler for scheduling the LEs of the DCHL to execute applications in accordance with inherited application priorities". At least the aspect of the "inherited application priorities" is not found in the independent claims of copending Application No. 10/740,036 (an amendment for which is being filed on even date with this amendment).

Claims 19-22 are rejected under 35 USC 101, the Examiner stating that these claims fail to tangibly embody or include any recited hardware.

Claim 19 has been amended to recite in several locations a "hardware logic element", thereby rendering the rejection moot. Note that the logic elements 10 are part of the Dynamic Configurable Hardware Logic (DCHL) 50, which forms a part of the Hardware Layer 70 (see, for example, Figure 4A).

The Examiner has now rejected claims 1, 2, 4, 5, 7, 8, 10, 11, 13, 14, 16, 19 and 21 under 35 U.S.C. 102(e) as being unpatentable over Johnson, U.S. Patent No. 6,834,315. The Examiner has also rejected dependent claims 6, 12, 17, 18, 20 and 22 under 35 U.S.C. 103(a) as being unpatentable Hoskins, US Patent No.: 6,789,132 in view of Johnson. The rejections are respectfully disagreed with, and are traversed below.

It is noted that when making the rejections under 102(e) and 103(a) that the Examiner cites Johnson and Hoskins, but then in the argument section refers repeatedly to "Jones". The rejection is thus not understood and should be clarified in a non-final office action, or withdrawn altogether.

Turning now to the rejection of claims 1, 7 and 13, it is noted that each of these claims recites in a somewhat similar fashion, as in claim 1:

"interposed between said OS and said DCHL layer, a TiEred Multi-media Acceleration Scheduler (TEMAS) that cooperates with the OS scheduler **for scheduling the LEs of the DCHL to execute applications**".

In making the rejection the Examiner is equating the logical unit numbers (LUNs) 16a,b,c of Johnson with the claimed LEs. This is clearly erroneous. As is well known, a LUN is a unique identifier used on a SCSI bus to distinguish between devices that share the same bus, and is considered a conceptual division (a subunit) of a storage disk or a set of disks (see the attached page (Exhibit A) obtained from www.expertglossary.com, as well as numerous other similar LUN definitions that can be found in print or on the web). As Johnson says in col. 1, lines 21-24, an application executing on a host may be assigned to use a particular logical volume in a storage subsystem, "also referred to as a Logical Unit Number (LUN)".

Clearly, the Logical Unit Numbers (LUNs) 16a,b,c of Johnson, which are merely separately identified application storage areas in the storage subsystem 14, are not equivalent to the Logic Elements (LEs) that are scheduled to "execute" applications. That is, there is no disclosure in Johnson that the LUNs 16a,b,c execute anything, instead they are merely defined regions in the storage subsystem.

For at least this one reason alone the independent claims 1, 7 and 13 are not anticipated by Johnson under 35 U.S.C. 102(e), and are also not suggested or rendered obvious in view of Johnson. These claims are thus clearly patentable over Johnson, as are all claims that depend from these claims for at least this one reason alone.

A merely clarifying amendment has been made to independent claim 19 to recite, in part:

"where said logic element scheduler responds to receipt of scheduling events to configure and reconfigure at least some of the plurality of hardware logical elements such that at one time a particular hardware logic element is scheduled for operation with a first algorithm logic for executing the first algorithm logic, and at another time the same particular hardware logic element is scheduled for operation with a second, different algorithm logic for executing the second, different algorithm logic".

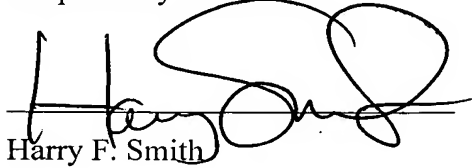
Independent claim 19 is thus also not anticipated by Johnson under 35 U.S.C. 102(e), and is also not suggested or rendered obvious in view of Johnson. As claim 19 is thus clearly patentable over Johnson, then the claims 20-22 are also clearly allowable over Johnson for at least this one reason alone.

As all of the independent claims are clearly allowable over Johnson alone, or Johnson in

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view of Hoskins, an early notification of the allowance of all of pending claims 1, 2, 4-8, 10-14 and 16-22 is earnestly solicited.

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EXHIBIT A

Technology (21,159) : A B C D E F G H I J K L M N O P Q R S T U V W X Y Z # Definitions for LUN (Logical Unit Number)

LUN (Logical Unit Number)

In [All](#) > [Technology](#) > [Storage](#)

- A unique identifier used on a [SCSI](#) bus to distinguish between devices that share the same bus.
[Adaptec](#) - [Cite This Source](#) - [This Definition](#)
- A SCSI term that refers to a logical disk device composed of one or more physical disk mechanisms, typically configured into a RAID level.
[HP](#) - [Cite This Source](#) - [This Definition](#)
- A logical unit is a conceptual division (a subunit) of a storage disk or a set of disks. Logical units can directly correspond to a volume drive (for example, C: can be a logical unit). Each logical unit has an address, known as the logical unit number (LUN), which allows it to be uniquely identified.
[Microsoft](#) - [Cite This Source](#) - [This Definition](#)

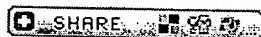
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- Logical Unit Numbers (LUN) is the number assigned to a logical unit (a [SCSI](#) protocol entity).
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